

# Design Summit ASIA



## RapidIO

Multi-Processor  
Embedded Interconnect

Switched | Scalable | Low Latency | Reliable

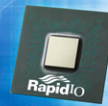
10 Gbps

20 Gbps

40 Gbps

100+ Gbps

ANY TOPOLOGY  
ANY PROCESSOR  
OPEN STANDARD



WIRELESS INFRASTRUCTURE | SERVER | HPEC | IMAGING | AEROSPACE | INDUSTRIAL

## Seoul Beijing Shanghai Tokyo

Learn directly from the leading vendors of RapidIO processors, switches, board subsystems, IP and management tools.

Sam Fuller, author of *RapidIO: The Embedded Systems Interconnect* will speak.

Learn about the Gen 3 (10xN) specification and future RapidIO Directions.

Admission is free, complimentary lunch and refreshments provided.

Register at: <http://rapidioasia.eventbright.com>

Registration closes on October 20<sup>th</sup>, 2012

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## Presenting Companies





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### AGENDA

9:00 am	<b>Registration</b>	
10:00 am	RapidIO Overview	<b>Sam Fuller</b>
10:20 am	RapidIO developments in advanced wireless systems: LTE-Advanced, Test Equipment, DSP and RF	<b>CommAgility</b>
11:00 am	FPGA Solutions	<b>Altera</b>
11:40 am	Integrated Processors with RapidIO	<b>Freescale</b>
12:20 pm	<b>Lunch</b>	
1:00 pm	RapidIO IP Offerings & Roadmap	<b>Mobiveil</b>
1:40 pm	Multicore DSP Processors for Wireless and Embedded Applications	<b>Texas Instruments</b>
2:20 pm	RapidIO Switching for Wireless and Cloud RAN Servers	<b>IDT</b>
3:00 pm	RapidIO Management and Diagnostic Tools	<b>FETCorp</b>
4:00 pm	<b>Wrap-up and Networking</b>	



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### Locations

Locations		
October 31 <sup>st</sup> 10 am – 4 pm	Seoul, Korea	Grand Intercontinental 521 Teheran Ro, Gangnam-Gu, Seoul, 135-732
November 2 <sup>nd</sup> 10am – 4 pm	Beijing, China	Park Plaza Beijing Science Park 25 Zhichun Rd, Haidian District, Beijing, CN
November 5 <sup>th</sup> 10 am – 4 pm	Shanghai, China	Ramada Plaza Pudong 18 Xin Jinqiao Road, Shanghai, CN
November 8 <sup>th</sup> 10 am – 4 pm	Tokyo Japan	Shinagawa Prince Hotel 10-30 Takanawa 4-Chrome, Minato-Ku, Tokyo 108-8611 Japan